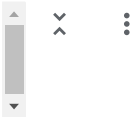




Section 1 of 8

OpenHW Group Member Survey Part B - Projects



This is Part B of a two-part survey for OpenHW Group members.

Part A asks about ecosystem benefits and governance.
Part B, this survey, asks about individual OpenHW projects. For projects on which you have no opinion, leave the question unfilled. At the end, you are asked about your priorities for future OpenHW activities.

Survey data will be anonymously collated and presented to members. The data will be used by OpenHW Staff, TG chairs, and members overall to assess effectiveness of projects and identify project improvements that can be made.

Each member company/organization should return a single response for Part A and Part B. Parts A and B can be filled in by the same person or different persons within the company/organization.

Thank you for taking the time to provide your valuable input.

After section 1 Continue to next section

Section 2 of 8

Member Information



Member information will not be included in summary of results presented to OpenHW members.

Company/Organization Name *

Short answer text

Name *

Name of person filling in survey on behalf of company/organization





Section 3 of 8

Core Projects



Feedback on Core projects which have currently passed the Project Concept gate.

Note that the OpenHW Group dashboard (<https://github.com/openhwgroup/core-v-docs/blob/master/program/dashboard/Dashboard.md>) contains information on each project that has achieved the Project Concept gate

CV32E40P

32bit 4 stage embedded core, released in December, 2020.
<https://github.com/openhwgroup/cv32e40p/blob/master/README.md>



	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

CV32E40P Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CV32E40PV2

32 bit 4 stage core with PULP extensions, in pre-launch (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CV32E40Pv2/CV32E40P%20v2%20Project%20Concept%20June%2028%202021.pptx>



	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

CV32E40PV2 Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CV32E40S

32bit 4 stage embedded core with security (S) features, in development <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CV32E40S/CV32E40S-PPL.md>



	Very Important	Important	Somewhat Importa...	Not Important
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Questions

Responses

Settings

CV32E40S Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CV32E40X

32bit 4 stage embedded core with extension (X) interface, in development (PA) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CV32E40X/CV32E40X-PPL.md>

Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐

CV32E40X Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CV32E41P

32 bit 4 stage embedded core supporting Zce compressed instruction, TRL3 target (proof of concept), in pre-launch (PC) <https://github.com/openhwgroup/core-v-docs/commit/58d022705138bd6b26c56b426117a0867252fffc>

Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐

CV32E41P Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CVA6

64/32bit 6 stage application class core, post- project launch (PL) <https://github.com/openhwgroup/core-v-docs/tree/master/program/Project%20Descriptions%20and%20Plans/CVA6>

Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐



Long answer text

CVA5

32bit 5 stage core, AKA Taiga, targeted at FPGA implementations, in pre-launch (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CORE-V%20CVA5/PC-Taiga-CVA5.md>

Very Important Important Somewhat Importa... Not Important

Importance to my c... ☐ ☐ ☐ ☐

Importance to the ... ☐ ☐ ☐ ☐

CVA5 Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CV32E20

32 bit 2 stage microcontroller, in pre-launch (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CVE20/Project%20Concept%20for%20CV32E20.md>

Very Important Important Somewhat Importa... Not Important

Importance to my c... ☐ ☐ ☐ ☐

Importance to the ... ☐ ☐ ☐ ☐

CV32E20 Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

After section 3 Continue to next section ▼

Section 4 of 8

Hardware Projects (Systems on Chip, Boards)

Feedback on hardware projects which have currently passed the Project Concept gate (or are expected to do so in the near future).

Note that the OpenHW Group dashboard (<https://github.com/openhwgroup/core-v>)





docs/blob/master/program/Project%20Descriptions%20and%20Plans/CORE-V%20MCU%20SoC/MCU%20PL%20Document.md

	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

CORE-V MCU Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

Verilator Model

A Verilator Model of CV32E40P, currently in post-launch (PL) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/Verilator%20Model/verilator-modeling-pl.md>

	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

Verilator Model Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

DEV-KIT

Development Kit Project comprising CORE-V MCU development board, and its marketing and distribution, currently in pre-concept

	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

DEV-KIT Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text



Software Projects

This section asks you to rate the importance to your company and to the OpenHW ecosystem of the software projects which have currently passed the Project Concept gate (or are expected to do so in the near future).

Note that the OpenHW Group dashboard (<https://github.com/openhwgroup/core-v-docs/blob/master/program/dashboard/Dashboard.md>) contains information on each project that has achieved the Project Concept gate

CORE-V IDE-CDT

Integrated Development Environment built on Eclipse CDT, currently in pre-launch, (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CORE-V%20IDE/CORE-V%20IDE%20preliminary%20project%20proposal.md>

Very Important Important Somewhat Importa... Not Important

Importance to my c... ☐ ☐ ☐ ☐

Importance to the ... ☐ ☐ ☐ ☐

CORE-V IDE-CDT Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

GNU GCC TOOLS

GNU compiler, linker, debugger and tools, currently in pre-launch, (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CORE-V%20GNU-Tools/core-v-gnu-tools-ppl.md>

Very Important Important Somewhat Importa... Not Important

Importance to my c... ☐ ☐ ☐ ☐

Importance to the ... ☐ ☐ ☐ ☐

GNU GCC TOOLS Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

LLVM TOOLS

LLVM compiler, linker, debugger and tools, currently in pre-launch, (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/LLVM%20Toolchain/llvm-toolchain-ppl.md>

Very Important Important Somewhat Importa... Not Important

Importance to my c... ☐ ☐ ☐ ☐





Questions

Responses

Settings

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

FREE RTOS

Real time operating system, currently in pre-launch, (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/Free%20RTOS/core-v-free-rtos-ppl.md>



Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐

FREE RTOS Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CORE-V Hardware Abstraction Layer (HAL)

HAL for support of RTOS (not Linux), currently in pre-launch (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CORE-V-HAL/Project-Concept-HAL.md>



Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐

CORE-V Hardware Abstraction Layer (HAL) Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CORE-V SDK

Software Development Kit supporting E family cores, currently in pre-launch, (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/SDK/sdk-project-concept.md>



Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐



After section 5 Continue to next section ▼

Section 6 of 8

Verification Projects and Approach



Feedback on Verification projects which have currently passed the Project Concept gate, as well as comment on the OpenHW Verification strategy

Note that the OpenHW Group dashboard (<https://github.com/openhwgroup/core-v-docs/blob/master/program/dashboard/Dashboard.md>) contains information on each project that has achieved the Project Concept gate

FORCE-RISCV Project

Open source instruction generator <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/FORCE-RISCV/FORCE-RISCV%20ISG%20project%20proposal.md>



Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐

FORCE-RISCV Project Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CORE-V-VERIF

The verification testbench approach and repository used to verify OpenHW Cores projects

Very Important

Important

Somewhat Importa...

Not Important

Importance to my c...

☐☐☐☐

Importance to the ...

☐☐☐☐

CORE-V-VERIF Comment

Provide any comment or suggestion you may have about CORE-V-VERIF, its priority, its suitability, or its progress

Long answer text





After section 6 Continue to next section

Section 7 of 8

Research and Specification Projects

Feedback on Research and Specification projects which have currently passed the Project Concept gate.

CV-VEC RESEARCH

Research program leading to a Vector Processor at TRL3, currently in pre-launch (PC) <https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CORE-V%20VEC%20Research/PPL%20proposal%20for%20Core-V-VEC%20Research%20Project.md>

	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

CV-VEC RESEARCH Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

CV-X-IF Specification

Specification of the X interface used for instruction offload to coprocessor, planned to be supported on both CVA6 and CV32E40X, currently in pre-launch (PC) https://github.com/openhwgroup/core-v-docs/blob/master/program/Project%20Descriptions%20and%20Plans/CV-X-IF/CV_X_Interface_project_concept.pdf

	Very Important	Important	Somewhat Importa...	Not Important
Importance to my c...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Importance to the ...	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

CV-X-IF Specification Comment

Provide any comment or suggestion you may have about this project, its priority, its suitability, or its progress

Long answer text

After section 7 Continue to next section





in an incubation stage.

Suggestion #1 for New OpenHW Group project

Indicate the nature of the potential project, why OpenHW should take it on, the timeframe in which it is needed and any other information you feel is relevant.

Long answer text

Suggestion #2 for New OpenHW Group project

Indicate the nature of the potential project, why OpenHW should take it on, the timeframe in which it is needed and any other information you feel is relevant.

Long answer text

Suggestion #3 for New OpenHW Group project

Indicate the nature of the potential project, why OpenHW should take it on, the timeframe in which it is needed and any other information you feel is relevant.

Long answer text

OpenHW Europe Working Group (EWG)

Indicate your interest in participating in the Europe-focused Eclipse Working Group that will incubate project concepts for the OpenHW Group, participate in European research projects, and ensure that European data sovereignty needs are met by OpenHW Group.

Very InterestedInterestedSomewhat Interest...Not Interested

Interest within my c...☐☐☐☐

OpenHW Asia Working Group

Indicate your interest in participating in an Asia-focused Eclipse Working Group that will incubate project concepts for the OpenHW Group, participate in Asian research projects, and ensure that Asian data sovereignty needs are met by OpenHW Group.

Very InterestedInterestedSomewhat Interest...Not Interested

Interest within my c...☐☐☐☐

Potential Interconnect TG

Indicate your interest in an Interconnect TG that will develop open source Network-on-Chip and Chiplet projects

Very InterestedInterestedSomewhat Interest...Not Interested

Interest within my c...☐☐☐☐





QuestionsResponsesSettings

Interest within my c...

